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BIPOLAR AND MOS ANALOG INTEGRATED CIRCUIT DESIGN ALAN B. GREBENE MICRO-LINEAR CORPORATION SUNNYVALE, CALIFORNIA A Wiley-Interscience Publication John Wiley & Sons New York Chichester Brisbane Toronto Singapore 628 PHASE-LOCKED LOOP CIRCUITS PART I: FUNDAMENTALS OF PHASE-LOCKED LOOPS 12.1

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Description. Integrated circuit design in silicon bipolar, MOS (Metal-Oxide-Semiconductor), and BiCMOS (Bipolar Complementary Metal-Oxide-Semiconductor) technologies for communications, sensor, instrumentation, data conversion, and power management applications.

ECE 4220 Analog Integrated Circuit Design | ECE | Virginia ...

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